

Electronic Acknowledgement Receipt

EFS ID:	1592149
Application Number:	10695336
International Application Number:	
Confirmation Number:	4852
Title of Invention:	Method of controlling grain size in a polysilicon layer and in semiconductor devices having polysilicon structures
First Named Inventor/Applicant Name:	Peter J. Geiss
Customer Number:	30449
Filer:	Jack P. Friedman/Kimberly Dwileski
Filer Authorized By:	Jack P. Friedman
Attorney Docket Number:	BUR920010184US2
Receipt Date:	14-MAR-2007
Filing Date:	28-OCT-2003
Time Stamp:	15:47:47
Application Type:	Utility

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	BUR920010184US2-finalamendment.pdf	205943	no	7

Warnings:

Information:	
Total Files Size (in bytes):	205943
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